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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**CERTIFICATION OF FACSIMILE TRANSMISSION**

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NAME OF INVENTOR(S): Clynes	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/862,523	
TITLE OF INVENTION: ON-CHIP 2D ADJUSTABLE DEFECTIVE PIXEL FILTERING FOR CMOS IMAGERS	
Filing Date: 5/22/2001	
TI FILE NO.: TI-32423	DEPOSIT ACCT. NO.: 20-0668
FAXED: 12-6-05 DUE: 11-9-2005 ATTY/SEC'Y: WDS/Nic	

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DEC 06 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Clynes
Serial No: 09/862,523
Filed: 5/22/2001
For: ON-CHIP 2D ADJUSTABLE DEFECTIVE PIXEL FILTERING FOR CMOS IMAGERS

Docket No: TI-32423
Examiner: Moe, Aung Soe
Art Unit: 2685

AMENDMENT UNDER 37 C.F.R. § 1.116

Assistant Commissioner For Patents
Washington, DC 20231

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Tommie Chambers

Dear Sir:

Responsive to the Office Action mailed September 9, 2005 in connection with
the above identified application, Applicants respectfully submit the following remarks.

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